

Please Direct All Correspondence to Customer Number **20995**

**NOTICE OF APPEAL**

FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND  
INTERFERENCES

Applicant : Gajadharsing et al.  
App. No : 10/551,324  
Filed : October 18, 2005  
For : SEMICONDUCTOR TRANSISTOR  
(DMOS) DEVICE FOR USE AS A  
POWER AMPLIFIER  
Examiner : Salerno, Sarah Kate  
Art Unit : 2814

**Mail Stop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Examiner dated April 16, 2009, finally rejecting Claims 1-11, each of which has been twice rejected.

**FEE CALCULATION**

FEES TYPE		FEES CODE	CALCULATION	TOTAL
Notice of Appeal	41.20(b)(1)	1401 (\$540)		\$540
Excess Claims > 20	10 - 20 = 0	1202 (\$52)	0 x 50 =	\$0
Independent > 3	1 - 3 = 0	1201 (\$220)	0 x 210 =	\$0
<b>TOTAL FEE DUE</b>				<b>\$540</b>

(X) The amount of \$540 will be paid via EFS Web.

Applicant further notes that a three-month extension of time was requested and paid for in connection with Applicant's Amendment After Final filed September 25, 2009.

Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Dated: October 14, 2009

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